

**CONTENT ADDRESSABLE MEMORY (CAM) DEVICES HAVING
CAM ARRAY BLOCKS THEREIN THAT PERFORM PIPELINED AND
INTERLEAVED SEARCH, WRITE AND READ OPERATIONS AND
METHODS OF OPERATING SAME**

Abstract of the Disclosure

5 Content addressable memory (CAM) devices according to
embodiments of the present invention conserve match line and bit line
power when CAM array blocks therein are searched. These CAM array
blocks are searched in a pipelined segment-to-segment manner to increase
search speed. The pipelined search operations may also be interleaved
with write and read operations in an efficient manner that reduces the
occurrence of pipeline bubbles.

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